

HDD Accelerator

The HDD accelerator board with 2.5 inch HDD has a 1 GB cache at the front end to increase the IOPS/throughput.

Development

- ▶ H/W: Design and development of circuits and FPGA
- ▶ S/W: OS porting and design and development of drivers and applications
- ▶ Brush up of specifications using a prototype, and role trading between H/W<->S/W on a function-by-function basis
- ▶ Design and construction of an inspection environment for mass

Features

- ▶ SATA1.0 3.5 inch HDD compatible specifications
- ▶ Equipped with a 1 GB cache (with ECC)
- ▶ Inbuilt backup power supply for cache protection
- ▶ NIOS II (OS: ul TRON) consisting of 1 chip is built into the FGPA
- ▶ The cache size can be easily increased by building a RAID environment with multiple drives
- ▶ Equipped with a cache algorithm
- ▶ Equipped with a function for cache protection during power outages (Inbuilt backup power supply function)
- ▶ External compatibility of the 3.5 inch HDD can be mounted as it is in a PC or HDD server

